

Abstract of the Disclosure

The present invention relates to a nonvolatile memory device including a write protected region, comprising a
5 program command processor, a write protected region setting unit and a write controller. The program command processor outputs a program command signal by decoding an external signal. The write protected region setting unit stores a region address corresponding to an inputted address when
10 the program command signal is activated, and outputs a write protect signal when the program command signal is inactivated. The write controller controls a cell corresponding to the region address not to perform a write mode when the write protect signal is activated.

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